



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Pavel Horsky and Ivan Koudar	§	Group Art Unit:	2836
		§		
Serial No.:	10/810,303	§		
		§	Examiner:	Ronald W. Leja
Filed:	March 26, 2004	§		
		§		
For:	Method and Device for Short	§	Atty. Dkt. No.:	BGC.0006US
	Circuit or Open Load Detection	§		(A2262-US)

**Mail Stop Issue Fee**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Dear Sir:

In the Examiner's Statement of Reasons for Allowance, prior art was characterized and claims 1 and 11 were paraphrased.

It is noted that the paraphrasing of claims 1 and 11 was not exact. The interpretation of claims 1 and 11 of the present application should be based on the express words of the claims themselves, and not on the inexact paraphrasing made in the Examiner's Statement of Reasons for Allowance.

Moreover, the Examiner's Statement of Reasons for Allowance also provided various characterizations of prior art and the statement that "[t]his cannot be interpreted as merely providing condition indication or interrupting the circuit operation." The characterization of the prior art and the statement quoted above does not appear to refer to the express language of claims 1 and 11. Therefore, claims 1 and 11 should not be construed in view of the Examiner's

Date of Deposit: August 28, 2007

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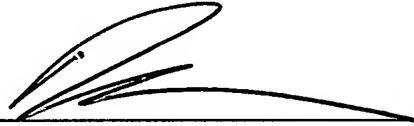
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characterizations made in the Examiner's Statement of Reasons for Allowance regarding language that does not appear in the claims.

Respectfully submitted,

Date: \_\_\_\_\_

8-28-2007



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